

PATENT NUMBER

U.S. UTILITY PATENT APPLICATION

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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/816133	F	327	333	2816	la

APPLICANTS
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 Katuyuki Yasukouchi

TITLE
 level shift circuit and semiconductor device

PTO-2040
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PREPARED AND APPROVED FOR ISSUE

ISSUING CLASSIFICATION									
ORIGINAL				CROSS REFERENCE(S)					
CLASS	SUBCLASS			CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION									
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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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<input type="checkbox"/> b) The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner) (Date)			ISSUE FEE	
				Amount Due	Date Paid
<input type="checkbox"/> c) The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)			ISSUE BATCH NUMBER	
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